

**REMARKS**

Claims 1-26 were pending in the subject application. This Amendment adds new claims 27-55 and cancels claims 1-26. However, Applicant reserves the right to re-assert claims 1-26 in the subject application or in a subsequent application. Reexamination and reconsideration of the subject application, as amended, is requested.

The July 5<sup>th</sup> Office Action rejected all claims under 35 U.S.C. 102(e) as being anticipated by Yasui et al. (US 5,784,039). Applicant notes that the July 5<sup>th</sup> Office Action is similar to a February 14, 2001 Office Action directed to the parent of the subject application. Applicant further notes the very extensive prosecution of the subject application. As the subject application is highly valuable, and as it contains allowable subject matter, Applicant wishes to add new claims that are allowable over the prior art of record.

Claim 27 is allowable at least for the reason that claim 27 recites a combination of elements including wherein the first voltage is greater than the data signal voltage and turns on the switching transistor; wherein the second voltage is near the data signal voltage; and wherein the reference voltage turns off the switching transistor.

Claim 38 is allowable at least for the reason that claim 38 recites a combination of elements including wherein the gate driver outputs the first voltage on a selected gate line during the application of a data signal in response to a scanning clock signal, wherein the gate driver outputs the second voltage on the selected gate line during the application of the data signal in response to a subsequent scanning clock signal, wherein the second voltage is applied after the first voltage.

Claim 51 is allowable at least for the reason that claim 51 recites a combination of elements including wherein the first voltage, which is greater than the potential of the data

signal, turns on a switching transistor; wherein the second voltage is substantially equal to the potential of the data signal; and wherein the reference voltage, which is less than the potential of the data signal, turns off the switching transistor.

None of the cited references teaches or suggests each and every element of the claims.

The Examiner's attention is directed to Figure 11A and to Figure 11B of the subject application (and to their supporting text). Figure 11A represents gate and data waveforms of US 5,587,722, while Figure 11B represents gate and data waveforms according to the principles of the present invention. The Examiner's attention is now directed to the falling edges of the gate drive signals. The subject application teaches techniques of controlling both the timing and the wave shapes of the fall. The Examiner's attention is further directed to the apparatus that achieves the wave shapes in Figure 11B, see Figures 4, 5, 7, 9, 10. The newly added claims include timing and magnitude limitations that are arrived at using selectively applied first and second voltages, controllers, switches, and other devices.

After carefully reading and considering Yasui et al. and Kusafuka et al. (which was relied on in earlier Office Actions to reject the subject application), the combination of claim elements in the newly added independent claims are believed allowable. Furthermore, claims 1-26 are cancelled, thus fully addressing all of the pending rejections.

Moreover, the dependent claims are believed to be allowable by virtue of their dependence on claims 27, 38, and 51, which are believed to be allowable.

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If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

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Respectfully submitted,

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